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Nguyen. Khanh
07/515961
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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10006924	FILING DATE 12/04/2001	CLASS 514	SUBCLASS 321	GAU 2817 1614	EXAMINER JARVIS William
**APPLICANTS: Stockstad Troy; 330 / 255 Nguyen. Khanh					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF 09/515961 03/01/2000 PAT 6,316,469 *					
(*)Data provided by applicant is not consistent with PTO records.					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	<input type="checkbox"/> DO NOT PUBLISH			RESCIND	<input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials			<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 60178-300402
TITLE : Low voltage rail-to-rail CMOS output stage					
U.S.DEP'T. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 10 Allowable 10 Nonallowable 0	Print Claims 0/0
Assistant Examiner		DRAWING	
Primary Examiner		Sheets Drawn 0 Total Drawings 0 Color Drawings 0	
PREPARED FOR ISSUE		Application Examiner	
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